



Form PTO-1419

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Chap 8

03 | 02 | 04

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U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (Including Author, Title, Date, Publication Pages, Etc.)

TII } J.Chen et al., "Design and Layout of a High ESD Performance NPN Structure for Submicron BiCMOS/Bipolar Circuits", IEEE Jnl 1996, 0-7803-2753-5/96, pp. 227-232.

Examen

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